


Day : Thursday
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Inventor Information for 10/777508


Inventor Name	City	State/Country
ALDEREGUIA, ALFREDO	CARY	NORTH CAROLINA
BEGUN, RALPH MURRAY	RALEIGH	NORTH CAROLINA
RICHTER, GRACE ANN	RALEIGH	NORTH CAROLINA

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Inventor Name Search Result

Your Search was:

Last Name = ALDEREGUIA

First Name = ALFREDO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07590978</u>	<u>5522064</u>	150	10/01/1990	DATA PROCESSING APPARATUS FOR DYNAMICALLY SETTING TIMINGS IN A DYNAMIC MEMORY SYSTEM	ALDEREGUIA, ALFREDO
<u>07591306</u>	<u>5226134</u>	150	10/01/1990	A DATA PROCESSING SYSTEM INCLUDING A MEMORY CONTROLLER FOR DIRECT OR INTERLEAVE MEMORY ACCESSING	ALDEREGUIA, ALFREDO
<u>07815737</u>	Not Issued	161	12/30/1991	PERSONAL COMPUTER WITH INTERLEAVED MEMORY SYSTEM FOR STORING A DATA PACKET USING A STAGGERED CAS PROTOCOL	ALDEREGUIA, ALFREDO
<u>07816203</u>	<u>5255374</u>	150	01/02/1992	BUS INTERFACE LOGIC FOR COMPUTER SYSTEM HAVING DUAL BUS ARCHITECTURE	ALDEREGUIA, ALFREDO
<u>07816693</u>	Not Issued	166	01/02/1992	COMPUTER SYSTEM WITH VARIED DATA TRANSFER SPEEDS BETWEEN SYSTEM COMPONENTS AND MEMORY	ALDEREGUIA, ALFREDO
<u>08177078</u>	<u>5481552</u>	250	12/30/1993	METHOD AND STRUCTURE FOR PROVIDING ERROR CORRECTION CODE FOR 8-BYTE DATA WORDS ON SIMM CARDS	ALDEREGUIA, ALFREDO
<u>08293411</u>	<u>5761533</u>	150	08/19/1994	COMPUTER SYSTEM WITH VARIED DATA TRANSFER SPEEDS BETWEEN SYSTEM	ALDEREGUIA, ALFREDO

				COMPONENTS AND MEMORY	
10777508	Not Issued	30	02/12/2004	Automated topology detection in a data processing system	ALDEREGUIA, ALFREDO
11064752	Not Issued	30	02/24/2005	Method and apparatus for testing, characterizing and monitoring a chip interface using a second data path	ALDEREGUIA, ALFREDO
11100680	Not Issued	30	04/06/2005	System and method for implementing a cable system	ALDEREGUIA, ALFREDO
11101272	7294786	150	04/06/2005	SYSTEM AND METHOD FOR MANAGING A CABLE IN A SERVER SYSTEM	ALDEREGUIA, ALFREDO
11143414	Not Issued	71	06/02/2005	Apparatus, system, and method for automatically detecting a cable configuration	ALDEREGUIA, ALFREDO
11170951	Not Issued	30	06/30/2005	Redundant 3-wire communication system and method	ALDEREGUIA, ALFREDO
11277868	Not Issued	30	03/29/2006	APPARATUS, SYSTEM, AND METHOD FOR ERROR ASSESSMENT OVER A COMMUNICATION LINK	ALDEREGUIA, ALFREDO
11305932	Not Issued	25	12/19/2005	Dynamic determination of signal quality in a digital system	ALDEREGUIA, ALFREDO
11315837	Not Issued	30	12/22/2005	Programmable throttling in blade/chassis power management	ALDEREGUIA, ALFREDO
11419533	Not Issued	30	05/22/2006	Communicating with Error Checking to a Device Capable of Operating According to an Address Prefix Serial Bus Protocol	ALDEREGUIA, ALFREDO

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Inventor Name Search Result

Your Search was:

Last Name = BEGUN

First Name = RALPH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>11092188</u>	Not Issued	30	03/29/2005	System management architecture for multi-node computer system	BEGUN, RALPH M.
<u>11312019</u>	Not Issued	30	12/20/2005	Authentication of I ² C bus transactions	BEGUN, RALPH M.
<u>11382080</u>	Not Issued	30	05/08/2006	SECURING LEASED RESOURCES ON A COMPUTER	BEGUN, RALPH M.
<u>07198890</u>	5175826	150	05/26/1988	DELAYED CACHE WRITE ENABLE CIRCUIT FOR A DUAL BUS MICROCOMPUTER SYSTEM WITH AN 80386 AND 82385	BEGUN, RALPH M.
<u>07198893</u>	Not Issued	168	05/26/1988	METHOD AND APPARATUS FOR SELECTIVELY POSTING WRITE CYCLES USING THE 82385 CACHE CONTROLLER	BEGUN, RALPH M.
<u>07198894</u>	5125084	150	05/26/1988	CONTROL OF PIPELINED OPERATION IN A MICROCOMPUTER SYSTEM EMPLOYING DYNAMIC BUS SIZING WITH 80386 PROCESSOR AND 82385 CACHE CONTROLLER	BEGUN, RALPH M.
<u>07358807</u>	Not Issued	166	05/31/1989	MICROCOMPUTER SYSTEM EMPLOYING ADDRESS OFFSET MECHANISM TO INCREASE THE SUPPORTED CACHE MEMORY CAPACITY	BEGUN, RALPH M.
<u>07358810</u>	5182809	150	05/31/1989	DUAL BUS MICROCOMPUTER SYSTEM WITH PROGRAMMABLE CONTROL OF LOCK FUNCTION	BEGUN, RALPH M.
<u>07359794</u>	5045998	250	06/01/1989	METHOD AND APPARATUS FOR SELECTIVELY POSTING	BEGUN, RALPH M.

				WRITE CYCLES USING THE 82385 CACHE CONTROLLER	
<u>07367653</u>	<u>5109506</u>	150	06/19/1989	MICROCOMPUTER SYSTEM INCLUDING A MICROPROCESSOR RESET CIRCUIT	BEGUN, RALPH M.
<u>07367828</u>	<u>5170481</u>	150	06/19/1989	MICROPROCESSOR HOLD AND LOCK CIRCUITRY	BEGUN, RALPH M.
<u>07368449</u>	<u>5146582</u>	150	06/19/1989	SYSTEM FOR USING A FIRST ADDRESS OF A BURST MODE DATA TRANSFER TO GENERATE EACH ADDRESS OF A PIPELINE MODE BEFORE CORRESPONDING BURST ADDRESSES ARE GENERATED	BEGUN, RALPH M.
<u>07696809</u>	<u>5327545</u>	250	05/07/1991	DATA PROCESSING APPARATUS FOR SELECTIVELY POSTIONG WRITE CYCLES USING THE 82385 CACHE CONTROLLER	BEGUN, RALPH M.
<u>07771528</u>	<u>5450559</u>	250	10/07/1991	MICROCOMPUTER SYSTEM EMPLOYING ADDRESS OFFSET MECHANISM TO INCREASE THE SUPPORTED CACHE MEMORY CAPACITY	BEGUN, RALPH M.
<u>07777844</u>	<u>5878256</u>	150	10/16/1991	METHOD AND APPARATUS FOR PROVIDING UPDATED FIRMWARE IN A DATA PROCESSING SYSTEM	BEGUN, RALPH M.
<u>07799486</u>	<u>5826075</u>	150	11/27/1991	AUTOMATED PROGRAMMABLE FIRMWARE STORE FOR A PERSONAL COMPUTER SYSTEM	BEGUN, RALPH M.
<u>07826733</u>	<u>5307482</u>	250	01/28/1992	COMPUTER, NON-MASKABLE INTERRUPT TRACE ROUTINE OVERRIDE	BEGUN, RALPH M.
<u>07861219</u>	Not Issued	163	03/31/1992	SELECTABLE PROGRAMMABLE FIRMWARE STORE FOR A PERSONAL COMPUTER SYSTEM	BEGUN, RALPH M.
<u>07905259</u>	<u>5459842</u>	150	06/26/1992	SYSTEM FOR COMBINING DATA FROM MULTIPLE CPU WRITE REQUESTS VIA BUFFERS AND USING READ-MODIFY-WRITE OPERATION TO WRITE THE COMBINED	BEGUN, RALPH M.

				DATA TO THE MEMORY	
<u>08067361</u>	<u>5381541</u>	250	05/26/1993	COMPUTER SYSTEM HAVING PLANAR BOARD WITH SINGLE INTERRUPT CONTROLLER AND PROCESSOR CARD WITH PLURAL PROCESSORS AND INTERRUPT DIRECTOR	BEGUN, RALPH M.
<u>08148955</u>	<u>5375084</u>	250	11/08/1993	SELECTABLE INTERFACE BETWEEN MEMORY CONTROLLER AND MEMORY SIMMS	BEGUN, RALPH M.
<u>08152221</u>	Not Issued	166	11/12/1993	COMPUTER SYSTEM HAVING ROM CAPABILITIES ON DIFFERENT BUSES	BEGUN, RALPH M.
<u>08387383</u>	Not Issued	166	02/13/1995	COMPUTER SYSTEM AND METHOD OF OPERATION THEREOF WHEREIN A BIOS ROM CAN BE SELECTIVELY LOCATABLE ON DIFFERENT BUSES	BEGUN, RALPH M.
<u>08706934</u>	<u>5680556</u>	150	09/03/1996	COMPUTER SYSTEM AND METHOD OF OPERATION THEREOF WHEREIN A BIOS ROM CAN BE SELECTIVELY LOCATABLE ON DIFFERENT BUSES	BEGUN, RALPH M.
<u>09953761</u>	Not Issued	161	09/17/2001	System and method for performing power management on a distributed system	BEGUN, RALPH MURRAY
<u>10777508</u>	Not Issued	30	02/12/2004	Automated topology detection in a data processing system	BEGUN, RALPH MURRAY
<u>08837756</u>	<u>5802393</u>	250	04/22/1997	COMPUTER SYSTEM FOR DETECTING AND ACCESSING BIOS ROM ON LOCAL BUS PERIPHERAL BUS OR EXPANSION BUS	BEGUN, RALPH MURRAY
<u>09053986</u>	<u>5884094</u>	250	04/02/1998	COMPUTER SYSTEM FOR DETECTING AND ACCESSING BIOS ROM ON THE LOCAL OR PERIPHERAL BUS	BEGUN, RALPH MURRAY

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Inventor Name Search Result

Your Search was:

Last Name = RICHTER

First Name = GRACE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
11100680	Not Issued	30	04/06/2005	System and method for implementing a cable system	RICHTER, GRACE A.
11101272	7294786	150	04/06/2005	SYSTEM AND METHOD FOR MANAGING A CABLE IN A SERVER SYSTEM	RICHTER, GRACE A.
11419533	Not Issued	30	05/22/2006	Communicating with Error Checking to a Device Capable of Operating According to an Address Prefix Serial Bus Protocol	RICHTER, GRACE A.
10777508	Not Issued	30	02/12/2004	Automated topology detection in a data processing system	RICHTER, GRACE ANN
11064752	Not Issued	30	02/24/2005	Method and apparatus for testing, characterizing and monitoring a chip interface using a second data path	RICHTER, GRACE ANN
11143414	Not Issued	71	06/02/2005	Apparatus, system, and method for automatically detecting a cable configuration	RICHTER, GRACE ANN
11170951	Not Issued	30	06/30/2005	Redundant 3-wire communication system and method	RICHTER, GRACE ANN
11277868	Not Issued	30	03/29/2006	APPARATUS, SYSTEM, AND METHOD FOR ERROR ASSESSMENT OVER A COMMUNICATION LINK	RICHTER, GRACE ANN

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